

# Navlakhi

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### JFET

(n-channel)  $V_p < 0$

D.C. Analysis

- KVL to GS loop:  $V_{GS} = \underline{\hspace{2cm}}$
- Shockley's eqn:  $I_D = I_{DSS} \left(1 - \frac{V_{GS}}{V_p}\right)^2$
- $I_D = \underline{\hspace{2cm}}$
- KVL to DS loop:  $V_{DS} = \underline{\hspace{2cm}}$

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(p-channel)  $V_p > 0$

D.C. Analysis

- KVL to S-G loop:  $V_{SG} = \underline{\hspace{2cm}}$
- Shockley's eqn:  $I_D = I_{DSS} \left(1 + \frac{V_{SG}}{V_p}\right)^2$
- $I_D = \underline{\hspace{2cm}}$
- KVL to DS loop:  $V_{DS} = \underline{\hspace{2cm}}$

### EMOSFET / DMOSFET

n-channel

p-channel

n-channel

p-channel

(n-channel) E:  $V_{TN} > 0$  D:  $V_{TN} < 0$

- KVL to GS loop:  $V_{GS} = \underline{\hspace{2cm}}$
- If saturation region:  $I_D = K_n (V_{GS} - V_{TN})^2$   
( $V_{DS} > V_{GS} - V_{TN}$ )  $I_D = \underline{\hspace{2cm}}$
- KVL to DS loop:  $V_{DS} = \underline{\hspace{2cm}}$

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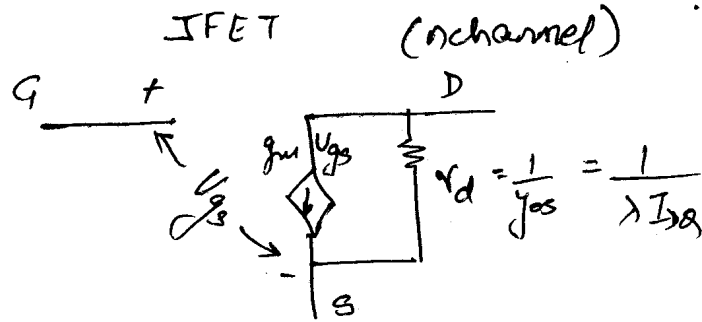
(p-channel) E:  $V_{TP} < 0$  D:  $V_{TP} > 0$

- KVL to GSG loop:  $V_{SG} = \underline{\hspace{2cm}}$
- If saturation region:  $I_D = K_p (V_{SG} + V_{TP})^2$   
 $V_{SD} > V_{SG} + V_{TP}$   $I_D = \underline{\hspace{2cm}}$
- KVL to SD loop:  $V_{SD} = \underline{\hspace{2cm}}$

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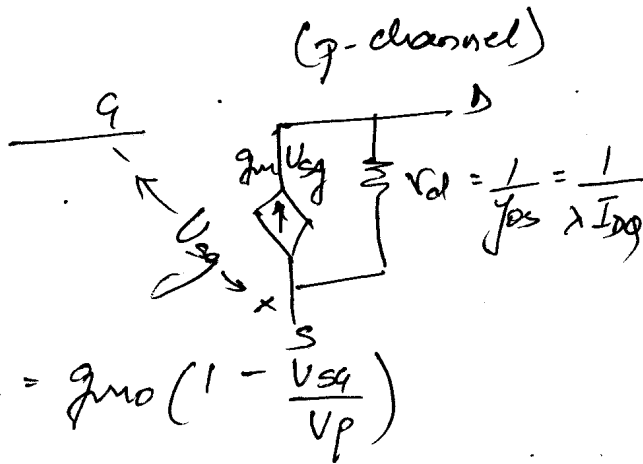
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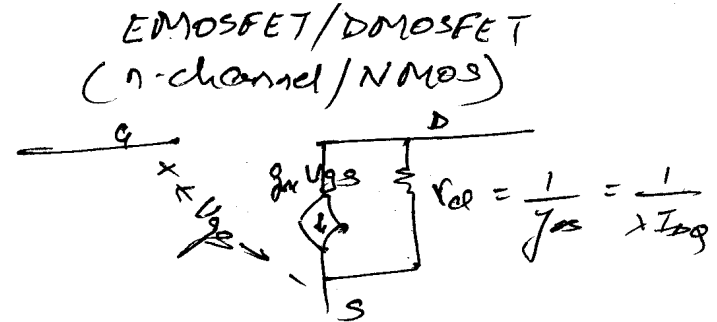
$$g_m = g_{m0} \left( 1 - \frac{V_{gs}}{V_p} \right)$$

$$g_{m0} = \frac{2 I_{DSS}}{|V_p|}$$



$$g_m = g_{m0} \left( 1 - \frac{V_{sg}}{V_p} \right)$$

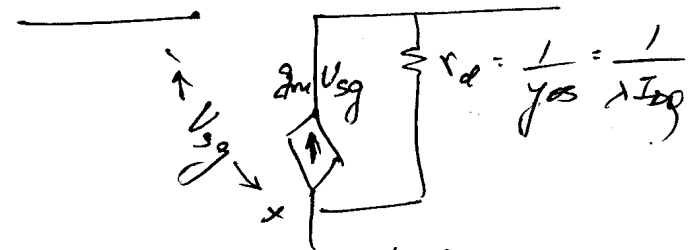
$$g_{m0} = \frac{2 I_{DSS}}{|V_p|}$$



$$g_m = 2 K_n (V_{gs} - V_{TN})$$

$$= 2 \sqrt{K_n I_D}$$

(p-channel)



$$g_m = 2 K_p (V_{sg} + V_{TP})$$

$$= 2 \sqrt{K_p I_D}$$

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